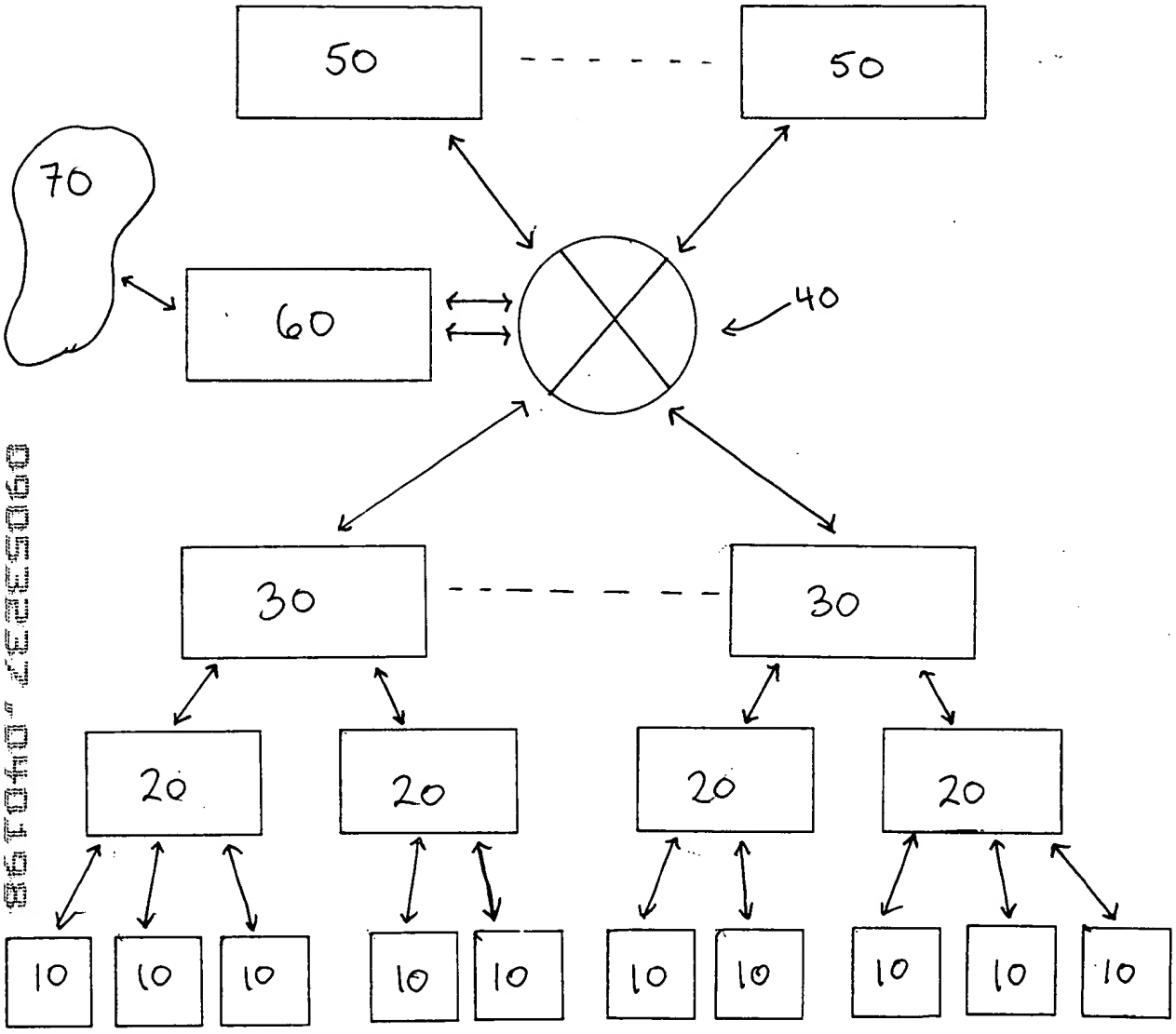


Figure 1: Router Architecture

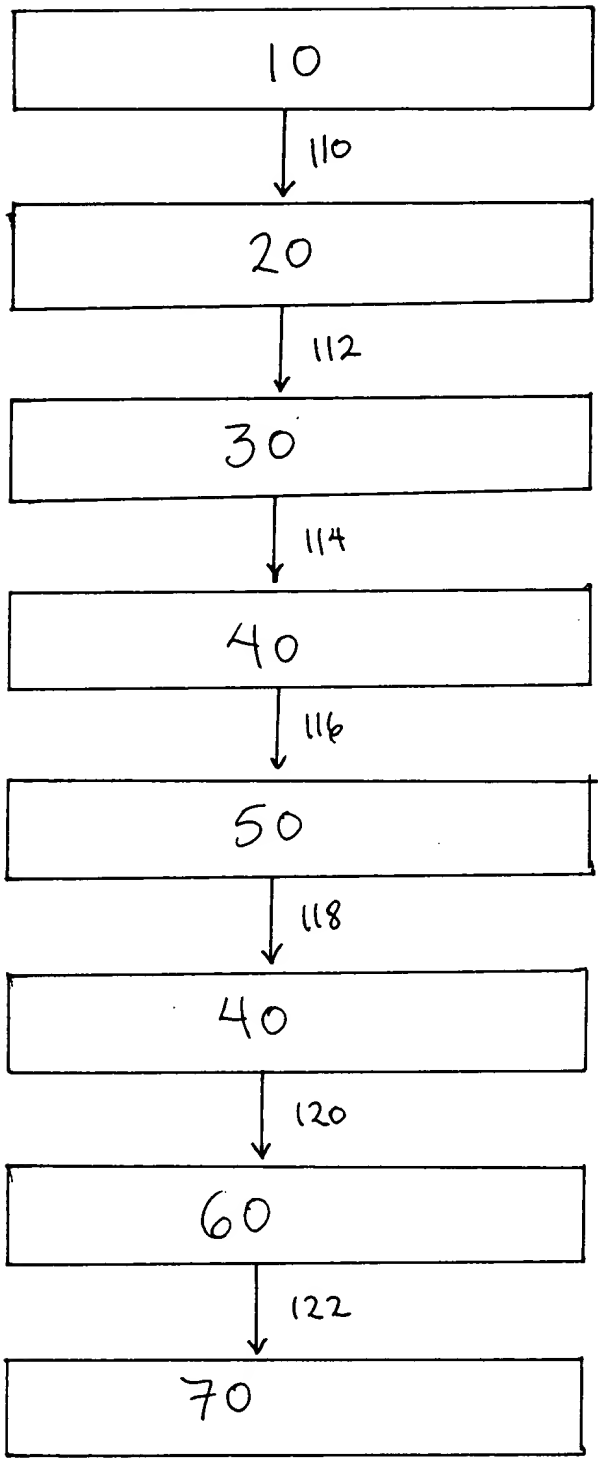


Figure 2: Sample Flow Diagram for a Data Packet

05053537.04018

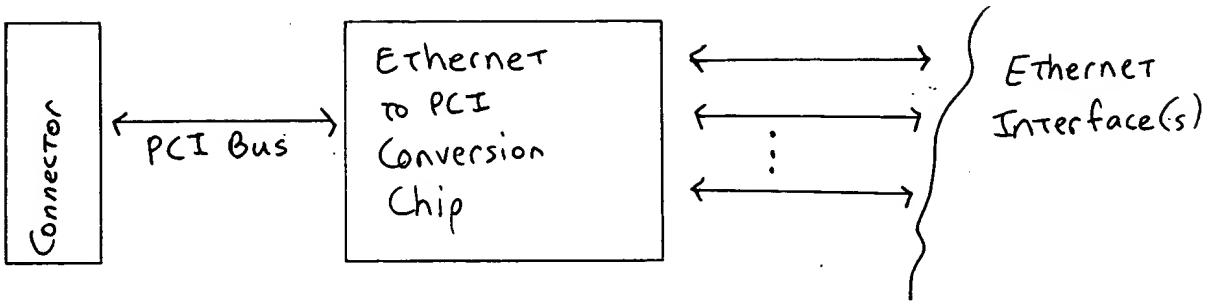


Figure 3a: Port Adaptor Design for an Ethernet Interface

Patent - 6,853,635

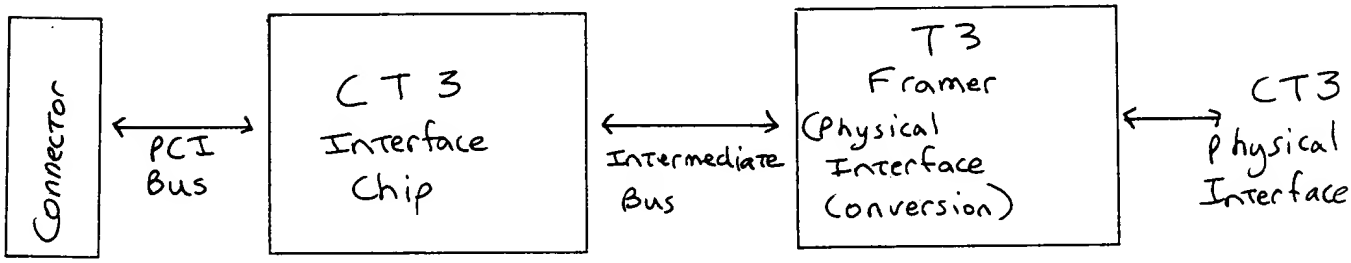


Figure 3b: Port Adaptor Design for a Channelized T3 Line (CT3) Interface

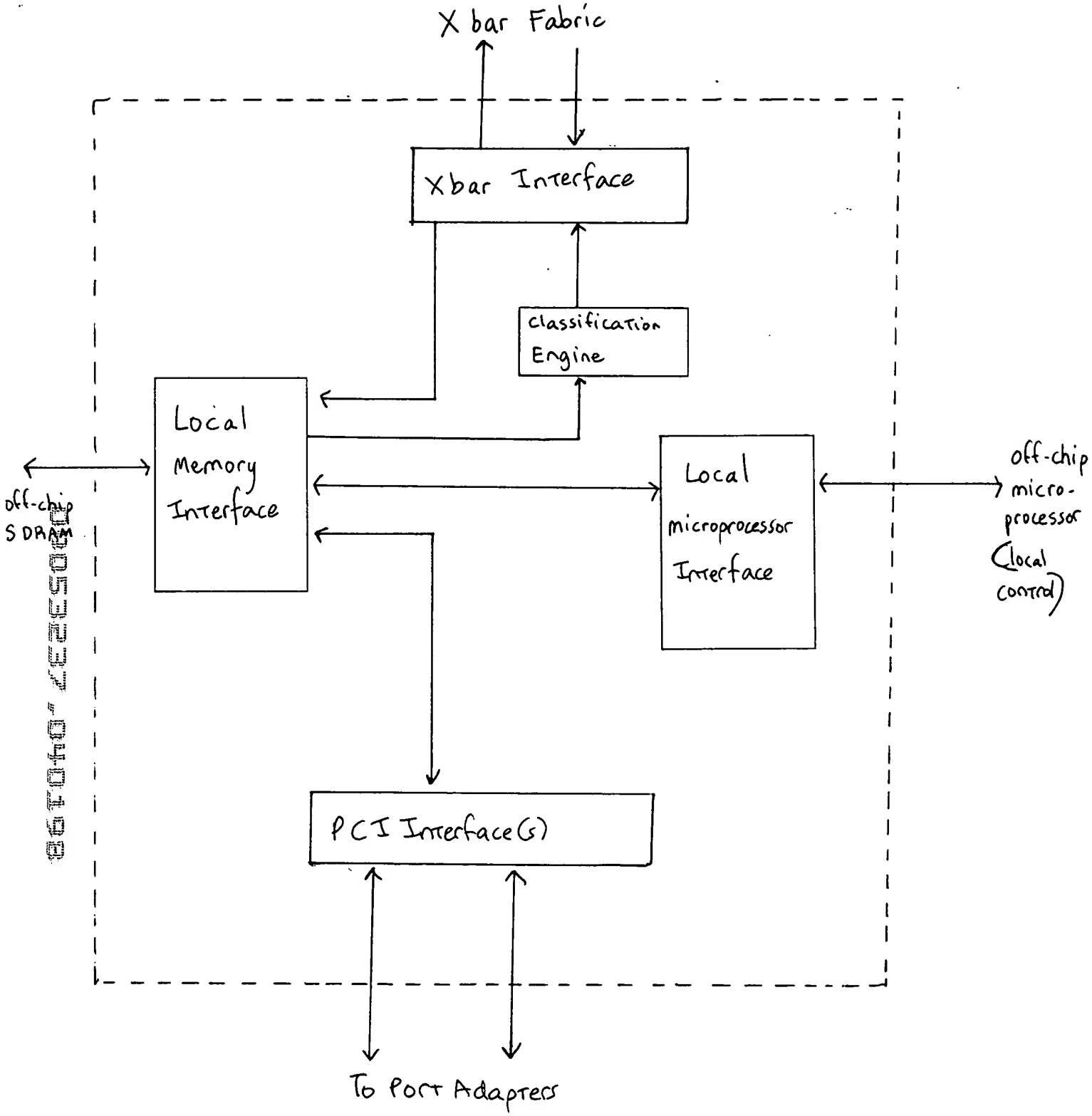


Figure 4: Data Compiler